

REMARKS

Claim 15 is amended, no claims are canceled, and claim 23 is added; as a result, claims 1-23 are now pending in this application.

No new matter is added through the amendments to claim 15. Support for the amendments to claim 15 may be found throughout the specification, for example but not limited to the specification on page 4 at lines 2-5, and also on page 4, line 29 through page 5, line 2.

Further, no new matter is added through new claim 23. Support for new claim 23 may be found throughout the specification, for example but not limited to the specification on page 8 at lines 12-15.

§102 Rejection of the Claims

Claims 15 and 17-20 were rejected under 35 U.S.C. § 102(b) as being anticipated by Uchimura et al. (U.S. 4,622,480).

Applicant submits that claim 15 includes one or more elements not taught by Uchimura et al., and thus claim 15 is not anticipated by Uchimura et al. For example, but not limited to this example, claim 15 as amended now recites,

producing **a substantially noise free differential current signal**, wherein a first current signal of the substantially noise free differential current signal is provided by a PMOS current mirror, and a second current signal of the substantially noise free differential current signal is provided by an NMOS current mirror.
(Emphasis added).

Applicant's representatives fail to find in Uchimura et al. a disclosure of "producing a substantially noise free differential current signal," as recited in amended claim 15. Further, since Uchimura et al. fails to disclose producing a substantially noise free differential current signal, Uchimura et al. cannot disclose "wherein a first current signal of the substantially noise free differential current signal is provided by a PMOS current mirror, and a second current signal of the substantially noise free differential current signal is provided by an NMOS current mirror," as also recited in amended claim 15.

Because Uchimura et al. fails to disclose at least these elements as recited in claim 15, Uchimura et al. fails to teach each of the elements included in claim 15, and so claim 15 is not anticipated by Uchimura et al.

Claims 17-20 depend from claim 15, and so include all of the elements recited in claim 15. Because Uchimura et al. fails to disclose each of the elements included in claim 15, Uchimura et al. cannot teach each of the elements included in claims 17-20, and so claims 17-20 are not anticipated by Uchimura et al.

Since claims 15 and 17-20 are not anticipated by Uchimura et al., the 35 U.S.C. §102 rejection of claims 15 and 17-20 cannot stand. Applicant respectfully requests withdrawal of the rejection, and reconsideration and allowance of claims 15 and 17-20.

Allowable Subject Matter

Claims 1-14, 16, and 21-22 are allowed. Applicant acknowledges the allowance of claims 1-14, 16, and 21-22.

Further, new claim 23 depends from allowed claim 16. Applicant respectfully requests allowance of new claim 23.

AMENDMENT UNDER 37 C.F.R. 1.116 – EXPEDITED PROCEDURE

Serial Number: 10/731,393

Filing Date: December 9, 2003

Title: POWER SUPPLIES NOISE DETECTOR FOR INTEGRATED CIRCUITS

Assignee: Intel Corporation

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Conclusion

Applicant respectfully submits that the claims are in condition for allowance and notification to that effect is earnestly requested. The Examiner is invited to telephone Applicant's attorney at (612) 371-2132 to facilitate prosecution of this application.

If necessary, please charge any additional fees or credit overpayment to Deposit Account No. 19-0743.

Respectfully submitted,

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CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Mail Stop AF, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 20th day of June, 2006.

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